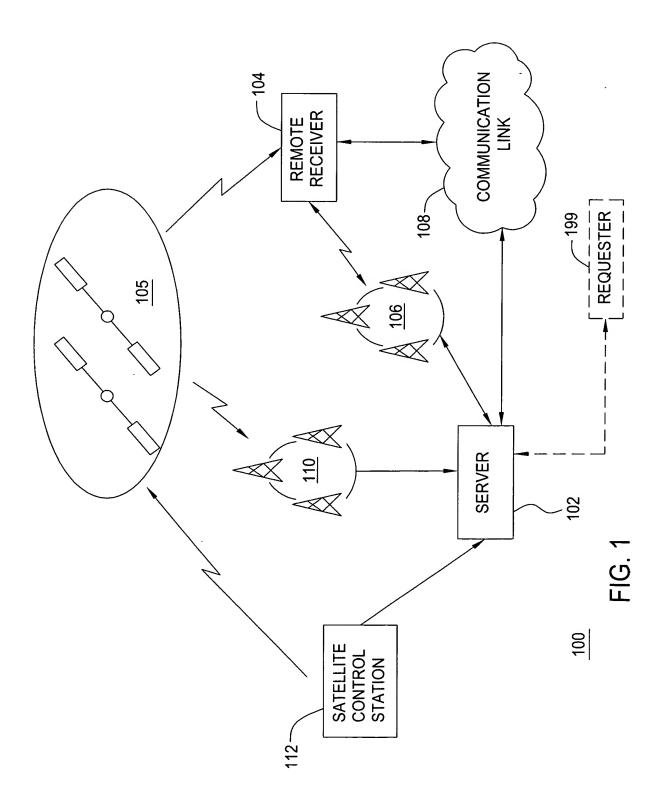
Inventors: van Diggelen et al.

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Inventors: van Diggelen et al.

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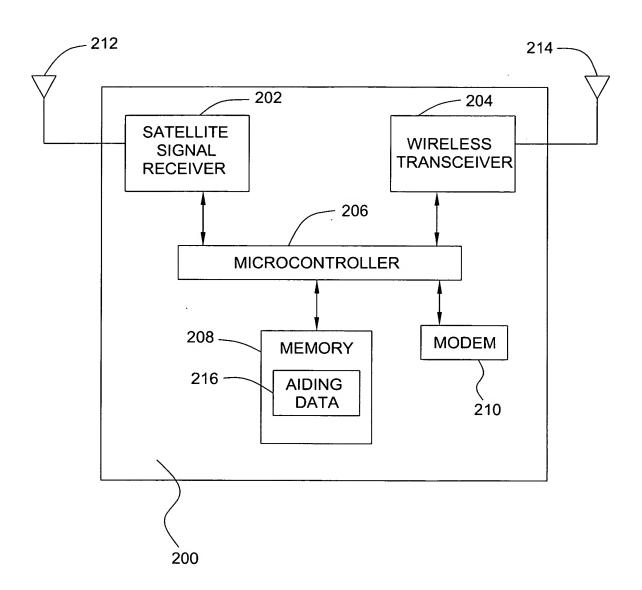
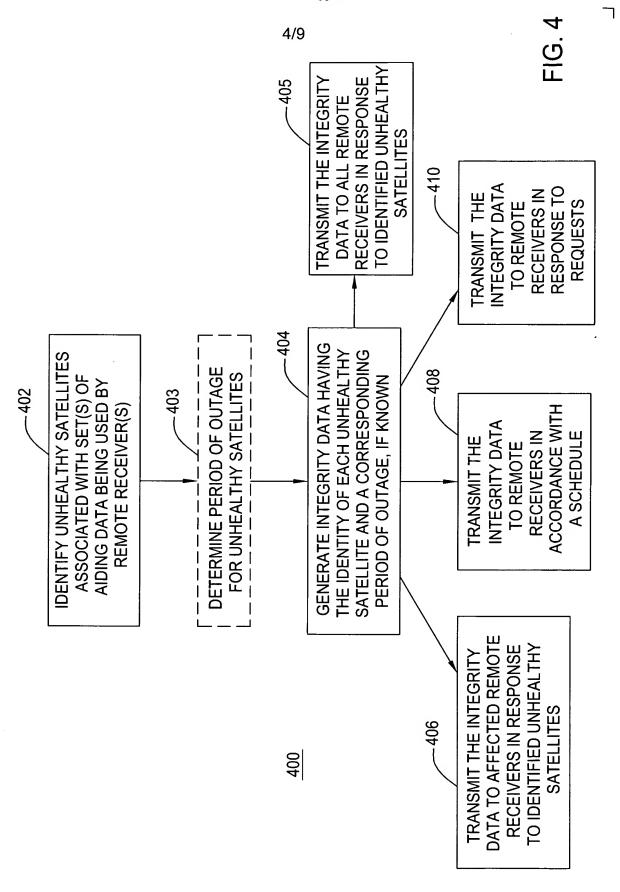


FIG. 2

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3/9 350 t2 + 4 HOURS **EXPIRATION** t3 + 2 DAYS t1 + 10 MIN DEVICE DATABASE ACQUISITION ASSIST **AIDING DATA EPHEMERIS** TYPE 170 DELIVERY TIME Ŋ \mathfrak{S} \Box 306 SUPPORT CIRCUITS **DEVICE ID** \sim က ~304 302 351~ 354~ 356 -INTEGRITY DATA CIRCUITS MEMORY 9 SPU SERVER CLOCK

Inventors: van Diggelen et al.



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Inventors: van Diggelen et al.

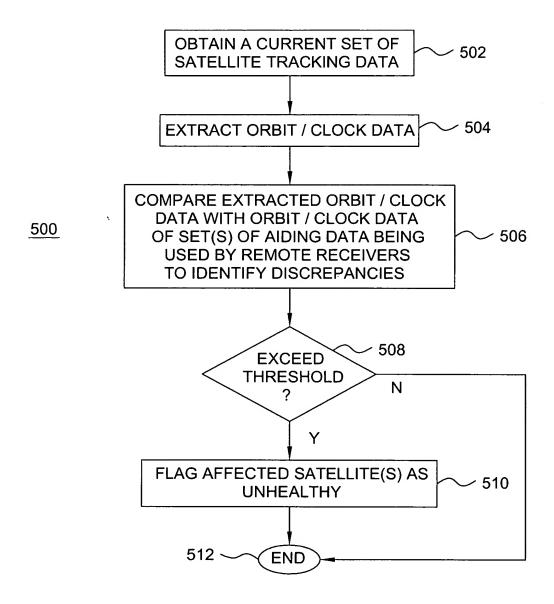


FIG. 5

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Inventors: van Diggelen et al.

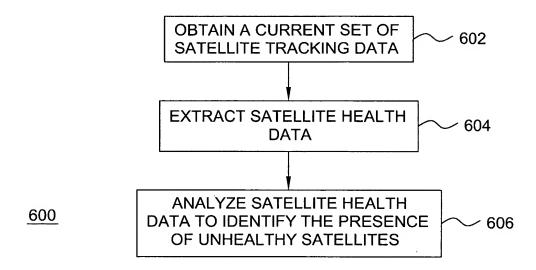


FIG. 6

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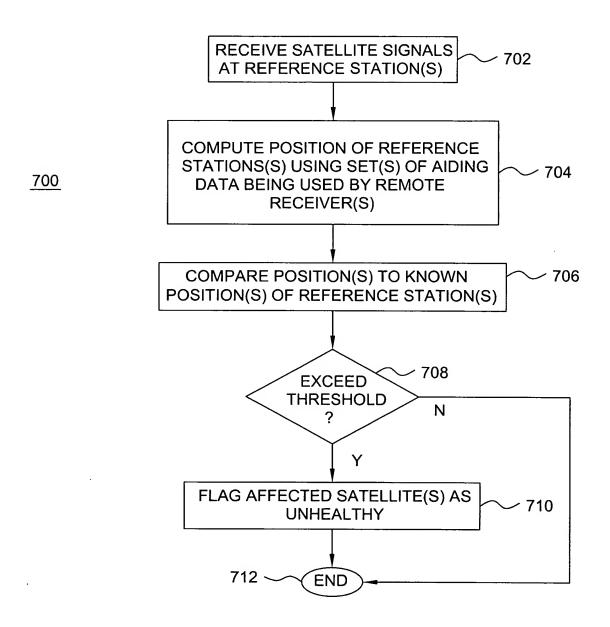


FIG. 7

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Inventors: van Diggelen et al.

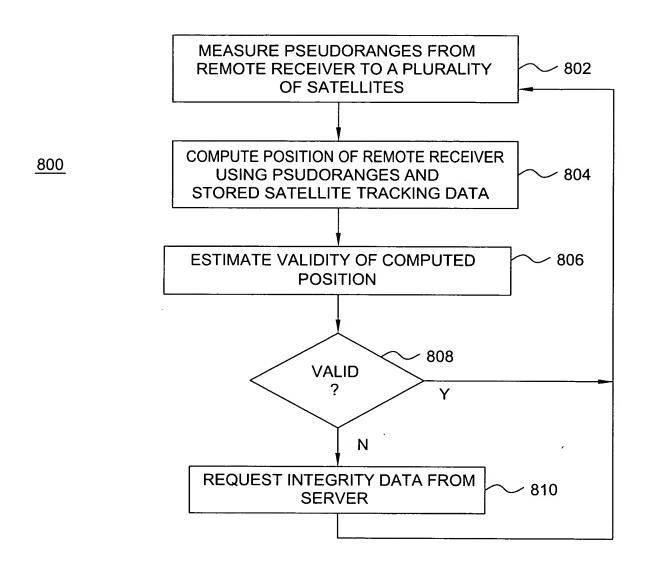


FIG. 8

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Inventors: van Diggelen et al.

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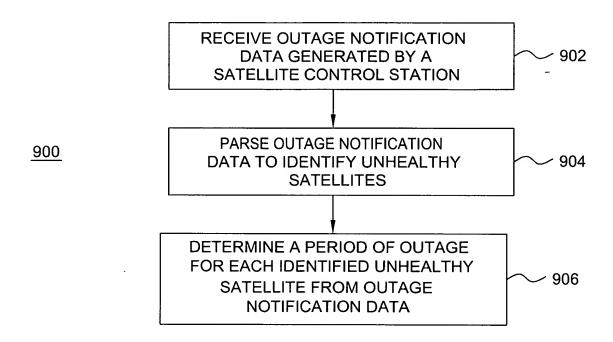


FIG. 9

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